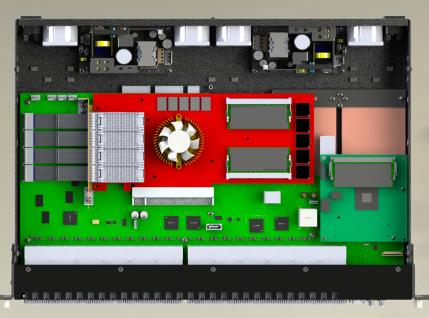
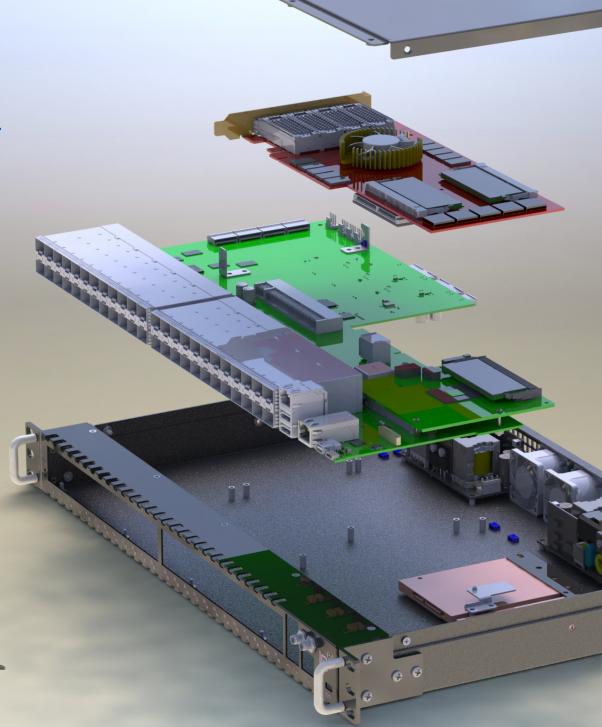


e4

FPGA board agnostic enclosure
Ethernet ports (including 25GbE)
PCIE and other serial links conversion into SFP+ ports
25/50/100GbE and 10/40GbE Layer 1 replication
10 ppb OCXO clock source
Wide range of supported CPUs
Per port signal monitoring tools
Wide range of supported OS
2 internal SSDs
Ultra-short depth, 1U
Zero configuration option





Ethernet ports (including 25GbE)	- 16 ports are 25.8Gbps capable
	- 32 ports are up to 12.5Gbps
	- Total 48 zSFP+ ports
	- Full signal recovery, regeneration, conditioning for each port
PCIe and other serial links conversion into	Increasing available number of high-speed links based on FPGA board's PCIe connector type (x4, X8, X16) and/or availability of
SFP+ ports	other serial links (FMC, SATA, etc.).
FPGA board agnostic enclosure	- Any PCIE form factor
	- Any PCIE FPGA board manufacturer
	- Any FPGA vendor
25/50/100GbE and 10/40GbE Layer 1	- Under 3ns replication latency
REPLICATION	- Both front panel input and FPGA board output can serve as a replication source
	- Replication scenarios: predefined as well as configurable through custom rules
Designated precision power control	- FPGA board independent power off / power on
	- current draw monitoring in real time
	- dedicated cooling
Ultra-fast startup time	- NETWORK AND PCIE: UNDER 3 SECONDS
	- Network ports' operation does not depend on installed OS, given that FPGA board can operate immediately after power-on
	without additional configuration from host OS
10 ppb TCXO clock source	via SMA, FPC and PCIe connectors
Dimensions : Ultra-short depth, 1U	16.9" X 1.71" X 12"
	allows to install two devices back to back in a standard 25" deep rack
Timestamping ports	2 on front panel.
Wide range of supported CPUs	Intel Atom/Xeon D/Core I are supported via COM Express type 6 or type 10 form factors.
2 internal 2.5 inch SSDs	Optional with COM Express modules that have on-board storage
Per port signal monitoring tools	- PRBS pattern generator, checker
	- NON-INTRUSIVE EYE MONITOR
Out-of-band management	- Dedicated Ethernet port
	- Micro USB serial console
Internal USB ports	Intended for FPGA debugging tools (JTAG adapters) with full power control
Wide range of supported operating systems	- Any up-to-date Linux distribution (Red Hat, CentOS, Ubuntu, etc.)
	- Windows
	- Dedicated ONL support: coming soon!
Zero configuration option (i.e. no mandatory	Device parameters, such as port speeds, switching matrix assignments, may be preconfigured at factory, or by the customer via
device configuration)	serial/out-of-band management ports without installing operating system on the Host CPU.
Optional installable LDA custom packages	- Monitoring
	- Diagnostics
	- Configuration, etc.

